Abstract of the Disclosure

In a heterogeneous multiprocessor system having a secure processor and a normal processor, a secure task and an unsecured task are allocated to respective processors. An encrypted code of the secure task is stored in a secure memory, and the secure memory verifies a signature using a public key of a certificate authority, and notifies the secure processor of the validity of the encrypted code. The secure processor fetches an encrypted instruction from the secure memory, and decrypts and executes the instruction.